

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

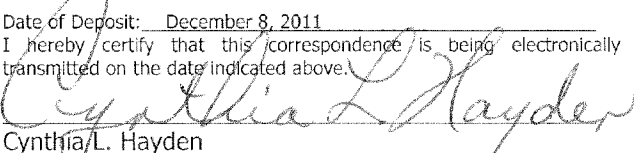
|                              |   |            |                   |
|------------------------------|---|------------|-------------------|
| In re Applicant:             | § | Art Unit:  | 2181              |
| Koichi Yamada et al.         | § |            |                   |
|                              | § | Examiner:  | Benjamin P. Geib  |
| Serial No.: 10/772,750       | § |            |                   |
|                              | § | Conf. No.: | 5683              |
| Filed: February 4, 2004      | § |            |                   |
|                              | § | Docket:    | ITL.2434US        |
| For: Sharing Idled Processor | § |            | P18129            |
| Execution Resources          | § |            |                   |
|                              | § | Assignee:  | Intel Corporation |

Mail Stop **RCE**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**PRELIMINARY AMENDMENT**

Sir:

Prior to the receipt of an office action, please amend the above-referenced patent application as follows:

Date of Deposit: December 8, 2011  
I hereby certify that this correspondence is being electronically  
transmitted on the date indicated above.  
  
Cynthia L. Hayden